EE 201 Lecture Notes

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Chapter 1

First Order Circuits

The current chapter contains only the discussion of two parallel capacitors. This discussion should be probably the section 3 of the finalized notes on first order circuits. Other sections will be written later (January 11, 2012).

1.1 Two Parallel Capacitors

Figure 1.1 shows two capacitors disconnected from each other at $t = 0^-$. When the switch is closed at t = 0, there will be current flowing in the loop and the capacitor voltages starts its movement. In this section, we analyze this simple looking circuit hiding some intricate results.

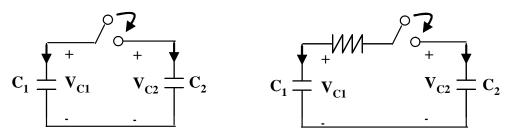


Figure 1.1: Two capacitor problem with and without resistor

We start the analysis of the circuit given on the right side of Figure 1.1. This circuit contains a resistor R, therefore its analysis is a straightforward exercise in the first order circuits. We assume that the following initial conditions, $V_{C1}(0^-) = V_1$ and $V_{C2}(0^-) = V_2$ for both circuits.

After the switch is closed, the circuit reduces to the one shown in Figure 1.2, which is a simple RC circuit with the time constant of $RC_1C_2/(C_1 + C_2)$). The

problem is the zero-input solution for the first order circuits. Hence its solution involves finding the initial conditions at $t = 0^+$ and the time-constant which we have already noted.

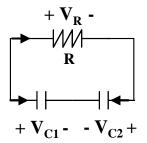


Figure 1.2: The equivalent circuit after switch closing

By an inspection of the circuit in Figure 1.2, the voltage of the resistor, $V_R(t)$, can be written immediately as follows:

$$V_R(t) = (V_1 - V_2)e^{-t/\tau}, \quad t \ge 0$$

We note one more time that $\tau = RC_1C_2/(C_1 + C_2))$ is the time-constant of the circuit.

From the circuit configuration, we have $I_{C1}(t) = -I_R(t) = -V_R(t)/R$ and $I_{C2}(t) = I_R(t) = V_R(t)/R$ where the current directions are given in Figure 1.2. Since the current of the capacitors is available for $t \ge 0$, we can find the voltages of capacitors using the (i,v) relation for the capacitors:

$$V_{C1}(t) = V_1 + \frac{1}{C_1} \int_{0^+}^t I_{C1}(t') dt', \quad t > 0$$

$$V_{C2}(t) = V_2 + \frac{1}{C_2} \int_{0^+}^t I_{C2}(t') dt', \quad t > 0$$

We do the calculation for $V_{C1}(t)$:

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(1.1)

$$V_{C1}(t) = V_1 + \frac{1}{C_1} \int_{0^+}^t I_{C1}(t') dt' \Big|_{I_{C1} = -V_R/R}$$

$$= V_1 - \frac{1}{RC_1} \int_{0^+}^t (V_1 - V_2) e^{-t'/\tau}, dt'$$

$$= V_1 + \frac{V_1 - V_2}{RC_1} \frac{e^{-t'/\tau}}{1/\tau} \Big|_{t'=0^+}^{t'=t}$$

$$= V_1 + \frac{V_1 - V_2}{RC_1} \tau \left(e^{-t/\tau} - 1 \right) \Big|_{\tau = RC_1C_2/(C_1 + C_2))}$$

$$= V_1 + (V_2 - V_1) \frac{C_2}{C_1 + C_2} \left(1 - e^{-t/\tau} \right)$$

$$= \frac{C_1 V_1 + C_2 V_2}{C_1 + C_2} + (V_1 - V_2) \frac{C_2}{C_1 + C_2} e^{-t/\tau}, \quad t \ge 0$$

Let's comment on the solution. By evaluating the solution at $t = 0^+$, we get $V_{C1}(t) = V_1$, this is what we expect from the continuity of the capacitor voltage. As $t \to \infty$, $V_{C1}(t)$ approaches the constant value of $\frac{C_1V_1+C_2V_2}{C_1+C_2}$. This fraction is the weighted average of the initial capacitor voltages according to the weights of C_1 and C_2 . For example when $C_1 = C_2$, the final value for $V_C(t)$ is $(V_1 + V_2)/2$, i.e the arithmetic average of initial conditions.

It is possible to repeat this calculation for $V_{C2}(t)$ but this is not recommended. Note that $V_{C2}(t) = V_{C1}(t) - V_R(t)$ due to the KVL constraint, then we have:

(1.2)
$$V_{C2}(t) = \underbrace{\frac{C_1 V_1 + C_2 V_2}{C_1 + C_2}}_{V_{\text{com}}} + (V_2 - V_1) \frac{C_1}{C_1 + C_2} e^{-t/\tau}, \quad t \ge 0$$

where $V_{\rm com}$ is the common DC voltage of the capacitors as $t \to \infty$.

This last relation can also be retrived by noting the symmetry of the circuit with respect to first and second capacitor. In other words, it is clear from Figure 1.1 that the capacitor on the left could have been labeled as C_2 at the very beginning. The circuit has left-right symmetry. Therefore switching all V_{C1} to V_{C2} , V_{C2} to V_{C1} , V_1 to V_2 , V_2 to V_1 in (1.1), we should get the solution for $V_{C2}(t)$.

Comment #1 (Voltage Balance): For the sake of simplicity, let's assume that $C_1 = C_2 = C$ and $V_1 = V_0$ and $V_2 = 0$. This means that we have an identical pair of capacitors one of which is charged at V_0 volts initially. For t > 0, the capacitor with V_0 volts gets discharged on the resistor. The other capacitor gets charged. This process continues until the final voltage of $V_0/2$ volts is reached. The time constant of the process is $\tau = RC/2$. One can interpret the situation as one of the capacitors transferring some of its charges to the other capacitor. This process of feeding the other capacitor, i.e. populating its plates with charges transferred from the other capacitor, continues until both capacitor have the same voltage. At that time instant $V_R = 0$ and the charge transfer stops! Note that the final value reached (the voltage balance) is independent of the resistance R. The resistance only effects the speed of the system.

Comment #2 (Energy Calculation): Lets calculate the energy dissipated on the resistor until the voltage balance is reached. This is the total amount of the energy dissipated on the resistor. There are two ways of calculating this value.

<u>Power Integral</u>: We can calculate the energy dissipated on the resistor by integrating the instantaneous power of the resistor.

$$W_{R} = \int_{0^{+}}^{\infty} \frac{[V_{R}(t')]^{2}}{R} dt'$$

$$= \int_{0^{+}}^{\infty} \frac{(V_{1} - V_{2})^{2} e^{-2t/\tau}}{R} dt'$$

$$= \frac{(V_{1} - V_{2})^{2}}{R} \frac{e^{-2t/\tau}}{-2/\tau} \int_{t'=0^{+}}^{t'=\infty}$$

$$= \frac{(V_{1} - V_{2})^{2}}{R} \frac{\tau}{2} \int_{\tau=RC_{1}C_{2}/(C_{1} + C_{2})}$$

$$(1.3) \qquad = \frac{(V_{1} - V_{2})^{2}}{2} \frac{C_{1}C_{2}}{C_{1} + C_{2}}$$

<u>Conservation of Energy</u>: The second method applies the conservation of energy principle to find the energy dissipated on the resistor. The energy dissipated is the difference of the initial stored energy and the final energy in the capacitors once the voltages are balanced.

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$$\begin{split} W_R &= \left\{ E_{C_1}(0^+) + E_{C_2}(0^+) \right\} - \left\{ E_{C_1}(\infty) + E_{C_2}(\infty) \right\} \\ &= \left\{ \frac{1}{2} C_1 V_1^2 + \frac{1}{2} C_2 V_2^2 \right\} - \left\{ \frac{1}{2} C_1 V_{\rm com}^2 + \frac{1}{2} C_2 V_{\rm com}^2 \right\} \\ &= \left\{ \frac{1}{2} C_1 V_1^2 + \frac{1}{2} C_2 V_2^2 \right\} - \frac{1}{2} (C_1 + C_2) V_{\rm com}^2 \bigg|_{V_{\rm com} = \frac{C_1 V_1 + C_2 V_2}{C_1 + C_2}} \\ &= \left. \frac{1}{2} C_1 V_1^2 + \frac{1}{2} C_2 V_2^2 - \frac{1}{2} \frac{(C_1 V_1 + C_2 V_2)^2}{C_1 + C_2} \right. \\ &= \left. \frac{1}{2} \frac{(C_1 + C_2) C_1 V_1^2 + (C_1 + C_2) C_2 V_2^2 - (C_1 V_1 + C_2 V_2)^2}{C_1 + C_2} \right. \\ &= \left. \frac{1}{2} \frac{C_1 C_2 (V_1^2 - 2 V_1 V_2 + V_2^2)}{C_1 + C_2} \right. \\ &= \left. \frac{1}{2} \frac{C_1 C_2 (V_1^2 - 2 V_1 V_2 + V_2^2)}{C_1 + C_2} \right. \\ &= \left. \frac{1}{2} \frac{C_1 C_2 (V_1^2 - 2 V_1 V_2 + V_2^2)}{C_1 + C_2} \right. \\ &= \left. \frac{1}{2} \frac{C_1 C_2 (V_1^2 - 2 V_1 V_2 + V_2^2)}{C_1 + C_2} \right. \end{split}$$

Note that the amount of energy dissipated on the resistor after switching is independent of the value of the resistor.

Comment #3 (On Resistance R): It has been observed that from the solution given in (1.1) that the final value of the capacitor voltage is independent of the resistance value R. This is indeed an interesting result. Furthermore, this observation leads to the fact that the amount of energy dissipated on the resistor in the time interval of $[0, \infty)$ is also independent of R. This last sentence has been explicitly verified by calculating the dissipated energy on the resistor in the previous comment.

Hence, we can conclude that if we have two capacitors in parallel, the final value of these capacitors is $V_{\text{com}} = \frac{C_1 V_1 + C_2 V_2}{C_1 + C_2}$ for any resistance value. The resistance value does not effect the final voltage of the capacitors, but affects the time constant of the circuit. It should be clear that as R gets smaller, the circuit acts faster and the final value is reached, i.e. 5 time constants, is reached in a shorter time.

Comment #4 (Case of No Resistance R = 0): Comment #3 implies that the final value for the capacitor is independent of the resistance value in the system. Then we take liberty of taking the resistance as $R = \epsilon$ where ϵ is arbitrary small positive constant. For any $\epsilon > 0$, however small, the comment #3 is valid. We assume that the same thing is also true for $\epsilon = 0$! This is small step for ϵ but a big step in functional analysis. In spite of this unjustified step; we accept this result as it is due to its practicality as explained below. Going back to the circuit given on the left side of Figure 1.1. This circuit does not contain any resistors. Its analysis can be confusing due to the lack of resistors. But once the comment #4 is accepted. The final value for the capacitors is

(1.4)
$$V_{com} = \frac{C_1 V_1 + C_2 V_2}{C_1 + C_2}$$

and there is some of loss energy due to switching. If you are uncomfortable with this result (loss of energy, where did the energy go?), you may consider that there is an ϵ amount of resistance somewhere in the system. Any resistance value is okay with us, you don't even need to tell us where it is!

1.1.1 Analysis of Two Parallel Capacitor Circuit With No Resistance

We present an analysis for the two parallel capacitor circuit when there is no resistance in the system. The analysis of this circuit can be troubling due to the discontinuity of the capacitors voltages at t = 0 and also due to the reduction in the stored energy before and after switching. The solution for this circuit has been explained in Comment #4 of the previous section. In this section, we present an alternative method and show that this alternative is consistent with the results of Comment #4.

Figure 1.3 shows the circuit when the resistance in series with the switch not present. The circuit-(II) in Figure 1.3 shows the equivalent circuit in which the initial voltages for the capacitors are shown with impulsive current sources. It should be noted that the capacitors in Circuit-(II) has no charge at $t = 0^-$, that is they have 0 volts initially. After the replacement of the initial voltages with the current sources, two capacitors on each side of the switch have the same voltage. Hence the complication in the analysis of the circuit due to the difference in the capacitor voltages is eliminated. We should say that the difficult is swept under the rug by including the impulsive sources in the system, but it should be clear that the circuit-(II) can be worked out like any other circuit containing impulsive sources.

Once the switch is closed, two capacitors have the common voltage of $V_{\rm com}$ as shown in Circuit-(III). This voltage is not known. To find this voltage, we keep the nodes between which the $V_{\rm com}$ defined fixed and combine all other components to simplify the circuit. By simple component combining, we get the circuit-(IV). Note that Circuit-(IV) and Circuit-(II) have the same $V_{\rm com}$ value, but all other circuit-(II) variables are invisible in Circuit-(IV).

It should be noted that $C_1 + C_2$ Farad capacitors has initial voltage of 0 volts, since C_1 and C_2 Farad capacitors in Circuit-(II) are initially empty. Then the

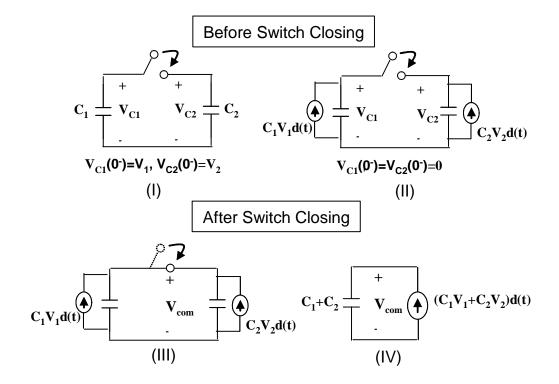


Figure 1.3: The original and equivalent circuits before and after switching

common voltage of two capacitors can be written as follows:

$$V_{\text{com}}(0^{+}) = V_{\text{com}}(0^{-}) + \frac{1}{C_{1} + C_{2}} \int_{0^{-}}^{0^{+}} (C_{1}V_{1} + C_{2}V_{2})\delta(t')dt'$$
$$= \frac{C_{1}V_{1} + C_{2}V_{2}}{C_{1} + C_{2}}$$

This result matches our earlier findings in the analysis of the circuit with the resistor.

A Second Way For V_{com} Calculation: Another way of finding $V_{\text{com}}(0^+)$ is recognizing that the circuit-(IV) represents an initially charged $C_1 + C_2$ Farad capacitor capacitor. The initial voltage of the $C_1 + C_2$ Farad capacitor at $t = 0^+$ is V_{com} and this value can be written as an impulsive current source having the functional form of $(C_1 + C_2)V_{\text{com}}\delta(t)$. Comparing $(C_1 + C_2)V_{\text{com}}\delta(t)$ with the current source in Circuit-(IV) gives us the relation for V_{com} voltage.

A Third Way via Conservation of Charge Principle: It is also possible to calculate the final voltage at $t = 0^+$ through the conservation of charge principle. The principle states that the total charge over the capacitors before and after should be same. (It is clear that the system is closed, therefore this statement is obviously true.)

$$Q_{\text{Total}}(0^{-}) = Q_{\text{Total}}(0^{+})$$

 $C_1V_1 + C_2V_2 = (C_1 + C_2)V_{\text{com}}$

 $Q_{\text{Total}}(t)$ appearing in the last equation refers to the total charge stored in the capacitors at time t. By solving for V_{com} , we get $V_{\text{com}} = \frac{C_1 V_1 + C_2 V_2}{C_1 + C_2}$.

Consistency of Energy Relations: It is also important to calculate the total energy in the system before and after switching. The initial and final energy in the system can be written as follows:

$$E_{\rm cap}(0^{-}) = \frac{1}{2}C_1V_1^2 + \frac{1}{2}C_2V_2^2$$

$$E_{\rm cap}(0^{+}) = \frac{1}{2}(C_1 + C_2)V_{\rm com}^2 = \frac{1}{2}\frac{(C_1V_1 + C_2V_2)^2}{C_1 + C_2}$$

You can easily check that $E_{\rm cap}(0^+) < E_{\rm cap}(0^-)$ unless $V_1 \neq V_2$. (This result can be shown by using convexity of $(\cdot)^2$ function.) The only other component in the system is the switch. Lets calculate the energy dissipated on the switch. Switch is the only suspect for the vanishing energy. (Here we are assuming that the switch is a large valued resistor whose resistance value is suddenly changed from 0 to ∞ at t = 0.)

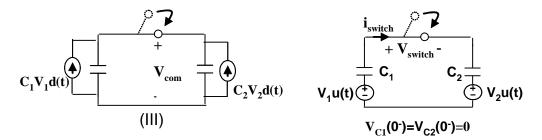


Figure 1.4: Circuit-(III) of Figure 1.3 and its equivalent

Figure 1.4 presents the Circuit-(III) from Figure 1.3 and another equivalent circuit. We have chosen to use the equivalent circuit given on the right hand side of Figure 1.4, since our interest has shifted from $V_{\rm com}$ calculation to $V_{\rm switch}$, $I_{\rm switch}$ calculation. From the equivalent circuit given in Figure 1.4, we can note the following:

$$V_{\text{switch}}(t) = (V_1 - V_2)u(-t) = \begin{cases} V_1 - V_2, & t < 0\\ 0, & t > 0 \end{cases}$$

The equation above represents the opening and closing of the switch. The current passing through the circuit is

$$I_{\text{switch}}(t) = \frac{C_1 C_2}{C_1 + C_2} (V_1 - V_2) \delta(t)$$

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The current equation can be checked by noting that the voltage across the series combination of C_1 and C_2 is $(V_1 - V_2)u(t)$. (Note that in the calculation related to the circuit variables of the switch, we simplify all the circuit components except the ones associated with the switch. Hence the branch of the switch is fixed and we are combining voltage sources, which are in series, and the capacitors, which are also in series.)

If the switch is treated as a circuit component, the energy dissipated on this component is:

$$E_{\text{switch}} = \int_{0^{-}}^{0^{+}} V_{\text{switch}}(t') I_{\text{switch}}(t') dt'$$

$$= \frac{C_1 C_2}{C_1 + C_2} (V_1 - V_2)^2 \underbrace{\int_{0^{-}}^{0^{+}} u(-t') \delta(t') dt'}_{1/2}$$

$$= \frac{1}{2} \frac{C_1 C_2}{C_1 + C_2} (V_1 - V_2)^2$$

The integral of $u(-t')\delta(t')$ in between 0^- and 0^+ is equal to 1/2 since "half of the area" under the impulse is nulled by u(-t). (Experienced readers should remember the fact that $\delta(t) = \delta(-t)$, i.e. $\delta(t)$ is an even function.)

From this calculation the energy dissipated on the switch, E_{switch} is found as $\frac{1}{2} \frac{C_1 C_2}{C_1 + C_2} (V_1 - V_2)^2$. By comparing this value with the power dissipated on the resistor given in (1.3), we can note that the amount of dissipated energy is identical in both cases. Hence $E_{\text{switch}} + E_{\text{cap}}(0^+) = E_{\text{cap}}(0^-)$ (as in the resistive case) and therefore energy is conserved.

This shows that there is no inconsistency in the analysis if the switch is treated as a circuit component, i.e. a time-varying resistor. If you uncomfortable with the operations with impulsives, i.e. impulsive current or "half the area" argument, you can always assume that there is an ϵ amount of resistance in series with the switch. You get the same result.