

# Allpass variable fractional delay filters by pole loci interpolation

H. Hacıhabiboğlu

**ABSTRACT:** A simple, accurate, and efficient variable fractional delay (VFD) IIR filter is presented. The method is based on the interpolation of pole loci of maximally-flat allpass fractional-delay filters. The responses of the proposed VFD filter for different fractional delay values are simulated and the approximation errors are shown to be very small.

*Introduction:* Fractional delay (FD) filters are digital filters that ideally have flat group delays emulating a non-integer delay. They are used in a variety of applications including sample rate conversion, synchronisation of telecommunications circuitry, and sound synthesis, where sub-sample accuracy is required [1]. Allpass IIR filters having maximally-flat group delays can be designed for this purpose [2]. Closed-form solutions exist for obtaining the coefficients of allpass IIR FD filters [1]. It is often necessary to have a variable fractional delay filter (VFD) that allows continuously varying the emulated delay. Different methods and allpass VFD filter structures have been proposed previously [3, 4, 5]. These methods suffer from one or more of the following problems: (i) The filter cannot be easily implemented using standard IIR filter structures making it impossible to use optimised DSP libraries, (ii) calculation of coefficients is computationally costly and not suitable for real-time operation, (iii) finite-precision arithmetic may cause stability issues, and (iv) transient errors are observed due to coefficient update. In order to overcome these, an allpass VFD filter using root displacement interpolation was proposed [6]. That method required the calculation and storage of the poles of two allpass FD filters modelling different fractional delay values  $D_1$  and  $D_2$ . Intermediate fractional delay values could then be obtained by bilinear interpolation of the poles of two filters. In order for the emulated delay to be accurate, the original filters had to be selected in such a way that  $|D_1 - D_2|$  is small. This requires the storage of the poles of several filters in order to get accurate results for a wide range of delays. It was also shown that implementing VFD filters as a cascade of second-order blocks significantly reduces transient errors due to filter coefficient update and also guarantees numerical stability [6]. In this letter, a new allpass VFD filter is proposed based on the interpolation of pole loci of reference allpass FD filters via fitting analytic, continuous functions of delay. The method allows emulating fractional delay values for a wide range of delays accurately.

*Allpass IIR VFD Filter structure:* The denominator coefficients of a maximally-flat group-delay allpass filter (also called Thiran filter), emulating a fractional delay  $D$  can be calculated using the closed-form expression [1]:

$$a_m = (-1)^m \binom{M}{m} \prod_{n=0}^M \frac{D+n}{D+n+m} \quad (1)$$

where  $D > -1$  is the fractional part of the delay that the filter emulates. The complex poles,  $r_i = R_i e^{\pm j\phi_i}$ , of the filter are conjugate symmetric and have reciprocal zeros. Therefore, the  $M^{\text{th}}$ -order maximally-flat group delay allpass filter can be specified only with  $M/2$  poles for  $M$  even or  $(M-1)/2 + 1$  poles if  $M$  is odd.

Let us consider an even-order Thiran filter. The filter can be implemented as a cascade of

$M/2$  second-order allpass sections. In this case, the denominator can be expressed as  $A(z) = \prod_{i=1}^{M/2} A_i(z) = \prod_{i=1}^{M/2} (1 - R_i e^{-j\phi_i} z^{-1})(1 - R_i e^{j\phi_i} z^{-1})$  where  $A_i(z)$  is the denominator of a second-order section, and  $R_i e^{\pm j\phi_i}$  is the pole pair associated with it. The denominator of the second-order section is a second-order polynomial of unit delay elements such that:

$$A_i(z) = 1 - 2R_i \cos(\phi_i) z^{-1} + R_i^2 z^{-2} \quad (2)$$

Here, the coefficients of the second order section are  $a_{i,0} = 1$ ,  $a_{i,1} = -2R_i \cos(\phi_i)$ , and  $a_{i,2} = R_i^2$ . When  $D \geq 0$  the loci of Thiran filter poles are smooth functions (see Fig. 1. in [6] for example). Within a given range,  $D_{\min} < D < D_{\max}$  of delays, modulus  $R_i$ , and the cosine of the angle  $\cos(\phi_i)$  of each pole can be approximated by analytic functions of the emulated delay, i.e.  $\rho_i(D) = R_i$  and  $\tau_i(D) = \cos(\phi_i)$ . The coefficients of the corresponding second-order section can thus be obtained using these analytic functions without recourse to (1). For this purpose, poles of reference Thiran filters are calculated on a fine grid of delay values and functional approximations are obtained. Fitted functions must allow simple calculation, and approximate the pole loci well. It was observed that the ratio of cubic polynomials provides a good fit to both  $\rho_i(D)$  and  $\tau_i(D)$ :

$$\rho_i(D) = \mathbf{f}_i \mathcal{D}^T / \mathbf{g}_i \mathcal{D}^T \quad (3)$$

$$\tau_i(D) = \mathbf{p}_i \mathcal{D}^T / \mathbf{q}_i \mathcal{D}^T \quad (4)$$

where  $\mathcal{D} = [1 \ D \ D^2 \ D^3]$ , is the delay vector,  $\mathbf{f}_i = [f_{i,0} \ f_{i,1} \ f_{i,2} \ f_{i,3}]$ ,  $\mathbf{g}_i = [g_{i,0} \ g_{i,1} \ g_{i,2} \ g_{i,3}]$ ,  $\mathbf{p}_i = [p_{i,0} \ p_{i,1} \ p_{i,2} \ p_{i,3}]$ , and  $\mathbf{q}_i = [q_{i,0} \ q_{i,1} \ q_{i,2} \ q_{i,3}]$  are polynomial coefficient vectors for a second-order section. This way, coefficients of each second order section are determined by a total of 12 coefficients for each arbitrary  $D_{\min} < D < D_{\max}$ . Calculation of coefficients for a single second-order section is shown in Fig. 1. It may be observed that  $\mathcal{D}$  can be calculated only once for a given fractional delay value  $D$ , and can be reused to obtain coefficients of all second-order sections. It may be noted that the proposed strategy is particularly well suited for single-instruction multiple-data (SIMD) processors.

**[Figure 1 about here.]**

*Example:* Performance of the proposed method was evaluated using 10<sup>th</sup>-order reference Thiran filters. Poles of Thiran filters modelling delays between  $0.01 \leq D \leq 2$  were calculated with 0.01 sample resolution. The functional forms for  $\rho_i(D)$  and  $\tau_i(D)$  were obtained as rational functions of cubic polynomials. It was observed that for all fitted functions the  $r^2$  metric representing the goodness of fit was always greater than 0.99.

Fig. 2 shows the phase delays of the VFD filter for different  $D$ . While the designed filters do not strictly have maximally-flat phase delays, they provide an excellent approximation.

**[Figure 2 about here.]**

Let us define two error metrics, the frequency response error (FRE), and phase delay error  $\epsilon(\omega; D)$ :

$$\text{FRE}(\omega; D) = 20 \log_{10} |H_{TH}(\omega; D) - H_{VFD}(\omega; D)| \quad (5)$$

$$\epsilon(\omega; D) = |\angle H_{TH}(\omega; D) - \angle H_{VFD}(\omega; D)| / \omega \quad (6)$$

where  $H_{TH}(\omega; D)$  and  $H_{VFD}(\omega; D)$  are the frequency responses of the reference Thiran filter and the proposed VFD filter emulating a fractional delay of  $D$ , respectively, and  $\angle H(\omega)$  denotes the phase response. FRE reflects the accuracy of the VFD filter in emulating the frequency response characteristics of the corresponding Thiran filter. The phase delay error, represents the accuracy of the phase delay response of the VFD filter in comparison with an actual Thiran filter.

Fig. 3 shows  $FRE(\omega; D)$  for different values of  $D$  across all frequencies. It may be observed that FRE is always less than about  $-50$  dB showing an excellent match between the original Thiran filters and the proposed VFD filter. Thiran filters and the proposed VFD filter have virtually the same frequency response at  $\omega = 0$  and  $\omega = \pi$ . The average FRE across all the simulated delays and frequencies is  $-64.9$  dB.

[Figure 3 about here.]

Fig. 4 shows  $\epsilon(\omega; D)$  for different values of  $D$  across different frequencies. The maximum error is always less than 0.0045 samples showing an excellent approximation of the emulated delay. The mean error is  $9.77 \times 10^{-4}$  samples across all the simulated delays and frequencies.

[Figure 4 about here.]

*Conclusions:* A simple, accurate, and effective allpass IIR variable fractional delay (VFD) filter was proposed. The proposed filter is based on the interpolation of the pole loci of reference Thiran filters. A design example was provided and the accuracy of the proposed filter was compared to that of reference Thiran filters. It was shown that the proposed VFD filter provided almost identical phase delays with the reference Thiran filters.

## References

- [1] T. I. Laakso, V. Välimäki, M. Karjalainen, and U. K. Laine, "Splitting the unit delay," *IEEE Signal Processing Mag.*, vol. 13, no. 1, pp. 30–60, 1996.
- [2] J.-P. Thiran, "Recursive digital filters with maximally flat group delay," *IEEE Trans. Circuit Theory*, vol. CT-18, no. 6, pp. 659–664, 1971.
- [3] M. Makundi, T. I. Laakso, and V. Välimäki, "Efficient tunable IIR and allpass filter structures," *Elect. Lett.*, vol. 37, no. 6, pp. 344–345, 2001.
- [4] J.-S. Park, B.-K. Kim, J.-G. Chung, and K. K. Parhi, "High-speed tunable fractional-delay allpass filter structure," in *Proc. 2003 IEEE Int. Symp. Circuits and Systems (ISCAS03)*, vol. 4, Bangkok, Thailand, May 2003, pp. 165–168.
- [5] C.-C. Tseng, "Design of 1-D and 2-D variable fractional delay allpass filters using weighted least-squares method," *IEEE Trans. Circuits and Syst. I, Fundam. Theory Appl.*, vol. 49, no. 10, pp. 1413–1422, October 2002.
- [6] H. Hacıhabiboğlu, B. Günel, and A. M. Kondoç, "Analysis of root displacement interpolation method for tunable allpass fractional-delay filters," *IEEE Trans. Signal Process.*, vol. 55, no. 10, pp. 4896–4906, October 2007.

**Author's affiliation**

Hüseyin Hacıhabibođlu (Centre for Digital Signal Processing Research, Division of Engineering, King's College London, London, WC2R 2LS, U. K. ), e-mail: [huseyin.hacihabiboglu@kcl.ac.uk](mailto:huseyin.hacihabiboglu@kcl.ac.uk)

## List of Figures

1	<i>Calculation of the coefficients of a second-order section. . . . .</i>	6
2	<i>Phase delays of the proposed VFD filter for various values of <math>D</math>. . . . .</i>	7
3	<i>Frequency response error, <math>FRE(\omega; D)</math>, (in dB) for different values of <math>D</math> across different frequencies. Colormap represents the magnitude of error. Lighter areas represents higher error. . . . .</i>	8
4	<i>Phase delay error, <math>\epsilon(\omega; D)</math>, (in samples) for different values of <math>D</math> across different frequencies. Colormap represents the magnitude of error. Lighter areas represents higher error. . . . .</i>	9

Figure 1:

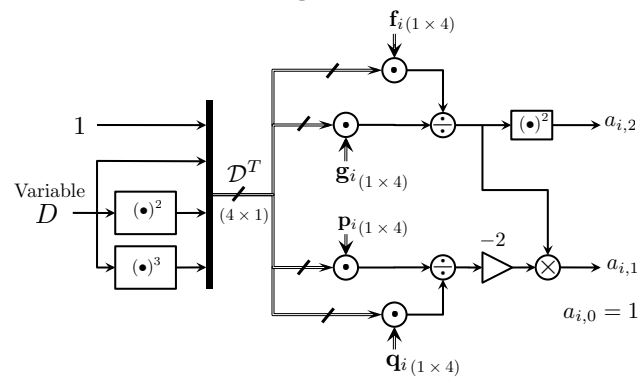


Figure 2:

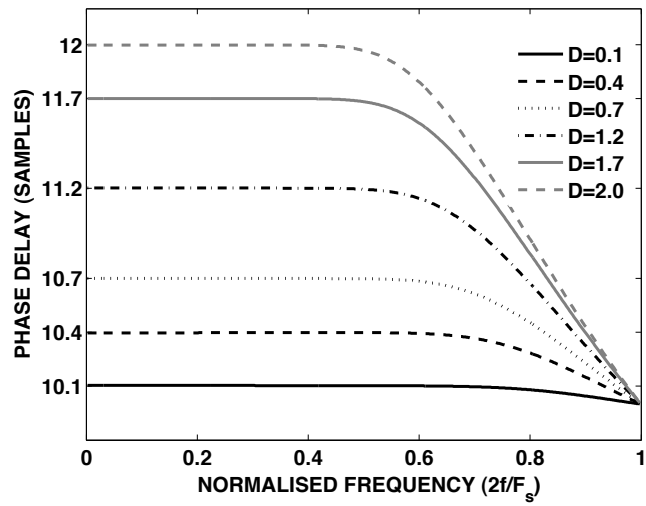


Figure 3:

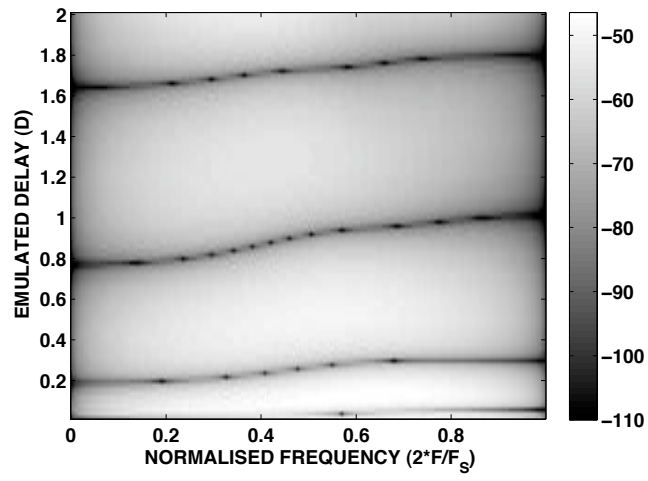




Figure 4:

