Özcan Yurt: The Development And Hardware Implementation Of A Dynamically Reconfigurable And Area Optimized Cyclic Redundancy Check Architecture (2013)

**DAROC: A Hardware CRC Calculator**

- Dynamic Reconfiguration for multiple-interface systems
- Configurable for all CRC Standards
- Minimized resource utilization
- Scalable architecture for parallelization

![Diagram of DAROC: A Hardware CRC Calculator](image)

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