Middle East Technical University  
Electrical and Electronics Engineering Department  
EE 348 Introduction to Logic Design  
2017-18 Spring Semester

Instructors  
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Assistants  
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Course Schedule  
(Sec. 1,5 IE-3, Sec.2: D-231, Sec.4: D-131)  
Tuesday: 11:40–12:30  
Thursday: 13:40–15:30

Textbook  

Other references:  

Grading  
Midterm Exam I 30%  
Midterm Exam II 30%  
Final Exam 30%  
Quiz+HW 10%  
Attendance Bonus

Week  Tentative Topics  
W1  Introduction, Numbers systems, Signed arithmetic  
W2  Boolean algebra, Operator precedence, Functions, Gates  
W3  Standard forms, Simplification of Boolean functions, Karnaugh map  
W4  Two level implementations  
W5  Analysis and design of combinational circuits  
W6  MSI circuits (Decoders, MUX, etc)  
W7  Basic FFs  
W8-11  Synchronous sequential ccts.  
W12  Registers, Serial mode of operation, counters, timing  
W13-14  Asynchronous sequential ccts.